

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5	(("20020122347") or ("20030151433") or ("6088255") or ("6218877") or ("63770952") or ("6476653").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/03 14:52
L2	1860	((delay locked loop) or DLL) and burst	US-PGPUB; USPAT; USOCR; EPO; JPO	ADJ	ON	2008/11/03 15:24
S1	22	"6088255"	US-PGPUB; USPAT	ADJ	ON	2008/04/16 14:03
S2	7	"6703682"	US-PGPUB; USPAT	ADJ	ON	2008/04/16 17:17
S3	15492	delay and ("transfer gate" or "transmission gate" or "pass gate" or "passgate" or "clocked inverter")	US-PGPUB; USPAT	ADJ	ON	2008/04/23 15:11
S4	3047	delay and ("transfer gate" or "transmission gate" or "pass gate" or "passgate" or "clocked inverter") and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/04/23 15:14
S5	332	delay circuit same ("transfer gate" or "transmission gate" or "pass gate" or "passgate" or "clocked inverter") and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/04/23 15:29
S6	233	variable delay and inverter and ("pass gate" or "passgate" or "clocked inverter" or "transmission gate") and register	US-PGPUB; USPAT	ADJ	ON	2008/04/23 16:05
S7	101	variable delay and inverter and ("pass gate" or "passgate" or "clocked inverter" or "transmission gate") and register and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/04/23 16:09

S8	153	variable delay and inverter and ("pass gate" or "passgate" or "clocked inverter" or "transmission gate") and (register or latch) and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/04/23 16:43
S9	35	phase comparator and latch and "clocked inverter" and "327".clas.	US-PGPUB; USPAT	ADJ	ON	2008/04/23 17:30
S10	5029	327/182,183,156-161,153.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/04/24 09:30
S11	1736	327/158,161.ccls.	US-PGPUB; USPAT	ADJ	ON	2008/04/24 09:31

11/3/2008 3:53:01 PM

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